

SPECIFICATION FOR APPROVAL

()	Preliminary Specification
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(♦) Final Specification

Title 10.1" WSVGA TFT LCD	
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Customer	Acer
MODEL	

SUPPLIER	LG Display Co., Ltd.		
*MODEL	LP101WSA		
Suffix	TLA1		

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE
/	
Please return 1 copy for yo	

APPROVED BY	SIGNATURE
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Products Engineerir LG Display Co.,	

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RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	Oct. 16. 2008	All	First Draft (Preliminary Specification)	-
0.1	Nov. 10. 2008	16~19	Mechanical Characteristics	-
1.0	Feb. 12.2009	7	BL_ON pin(@ Pin Map)	-
		26~28	EDID update	-
		13	Color Coordinates	-
		14	Gray Scale Specification	-
		4	Power Consumption	-
		19	Mechanical Characteristics	-

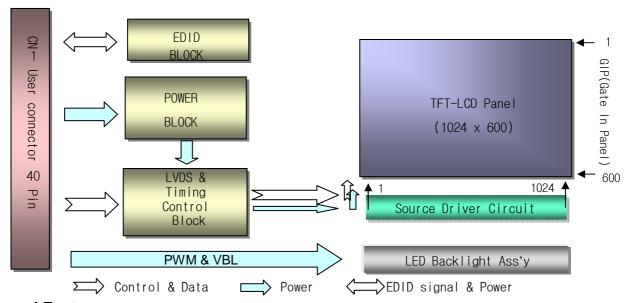


1. General Description

The LP101WSVGA is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 10.1inches diagonally measured active display area with WSVGA resolution(1024 horizontal by 600 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP101WS1 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP101WS1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP101WS1 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	10.1 inches diagonal			
Outline Dimension	235(H) × 143(V) × .5.2(D,Max.) [mm]			
Pixel Pitch	0.2175mmx0.2088mm			
Pixel Format	1024 horiz. By 600 vert. Pixels RGB strip arrangement			
Color Depth	6-bit, 262,144 colors			
Luminance, White	200 cd/m ² (Typ.5 point)			
Power Consumption	Total 2.55 Watt(Typ.) @ LCM circuit 0.82 Watt(Typ.), B/L input 1.73 Watt(Typ.) (W/O LED Driver)			
Weight	190g (Max.)			
Display Operating Mode	Transmissive mode, normally white			
Surface Treatment	Glare treatment of the front polarizer			
RoHS Comply	Yes			

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2. Absolute Maximum Ratings

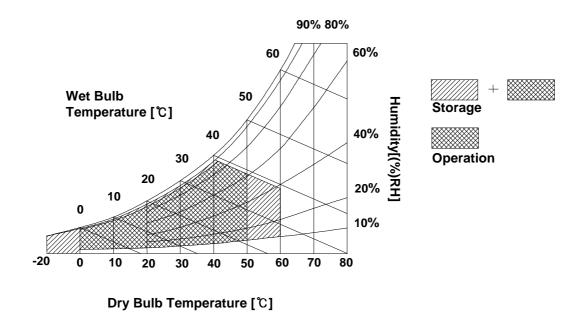
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
i didiliciei	Symbol	Min	Max	Office	Notes	
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

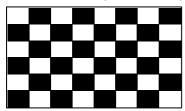
The LP101WS1 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the LED BL.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Cymhol		Unit	Notes		
Parameter	Symbol	Min	Тур	Max	Onit	Notes
MODULE :						
Power Supply Input Voltage	VCC	3.0	3.3	3.6	V_{DC}	
Power Supply Input Current	I _{cc}	-	250	280	mA	1
Power Consumption	Pc	-	0.82	0.93	Watt	1
Differential Impedance	Zm	90	100	110	Ohm	2
LED Backlight (With LED Driver):						
LED Driver (@12V)	P_{DRIVER}		0.12	0.14	Watt	
Operating Voltage	V_{LED}	-	28.8	30.6	V	
Operating Current per string	I _{LED}	-	20		mA	3
Power Consumption	P_BL	-	1.73	1.84	Watt	4
Life Time		10,000	-	-	Hrs	5

Note)

1. The specified current and power consumption are under the Vcc = 3.3V, $25^{\circ}C$, fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.



- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The typical operating current is for the typical surface luminance (L_{WH}) in optical characteristics. I_{LED} is the current of each LED's string, LED backlight has 3 strings on it.
- 4. The LED power consumption shown above does not include power of external LED driver circuit for typical current condition.
- 5. The life time is determined as the time at which brightness of LED is 50% compare to that of initial value at the typical LED current.

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3-2. Interface Connection

This LCD employs one interface connection, a 40 pin connector is used for the module electronics interface.

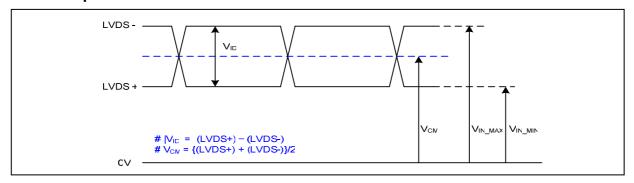
Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Din	Symbol	Pagarintian	, ,
Pin	Symbol	Description (Description)	Notes
1	CT1/NC	Connector Test/No Connection(Reserved)	
2	VDD	+3.3V Power Supply	
3	VDD	+3.3V Power Supply	
4	V _{EDID}	+3.3V EDID Power	1, Interface chips
5	NC	No Connection	1.1 LCD: SiW, 1port including LVDS Receiver
6	CLK _{EDID}	EDID Clock Input	1.2 System :
7	DATA _{EDID}	EDID Data Input	* Pin to Pin compatible with LVDS
8	RxIN0-	LVDS differential data input	2. Connector
9	RxIN0+	LVDS differential data input	2.1 LCD :I-PEX 20455-040E-0*
10	GND	Ground	(Locking type)
11	RxIN1-	LVDS differential data input	or equivalent
12	RxIN1+	LVDS differential data input	2.2 Mating:
13	GND	Ground	2.3 Connector pin arrangement
14	RxIN2-	LVDS differential data input	
15	RxIN2+	LVDS differential data input	40
16	GND	Ground	
17	RxCLKIN-	LVDS differential clock input	
18	RxCLKIN+	LVDS differential clock input	[LCD Module Rear View]
19	GND	Ground	[LOD Module Real view]
20	NC NC	No Connection	
21	NC NC	No Connection	
22	GND	Ground	
23	NC NC	No Connection	
24	NC NC	No Connection	
25	GND	Ground	
26	NC NC	No Connection	
27	NC NC	No Connection	
28	GND	Ground	
29	NC	No Connection	
30	NC	No Connection	
31	VLED_GND	LED Ground	
32	VLED_GND	LED Ground	
33	VLED_GND	LED Ground	
34	CT2/NC	Connector Test/No Connection(Reserved)	
35	S_PWMIN	System PWM signal input	
36	BL_ON	LED Enable[Note 1]	
37	NC	No Connection	
38	VLED	+4.5V~+21V LED Power Supply	
39	VLED	+4.5V~+21V LED Power Supply	[Note 1]
40	VLED	+4.5V~+21V LED Power Supply	On: 2.0V↑,Off:0~0.4V
,0	l vee	17.0 V 12 I V LLD I OWOI Oupply	



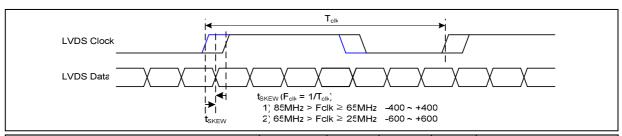
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	600	mV	-
LVDS Common mode Voltage	V _{CM}	0.6	1.8	V	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

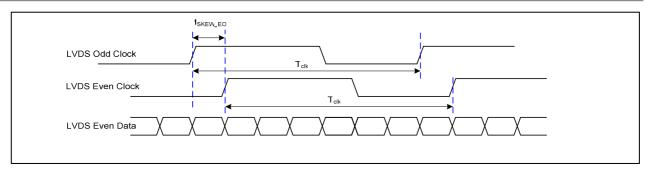
3-3-2. AC Specification



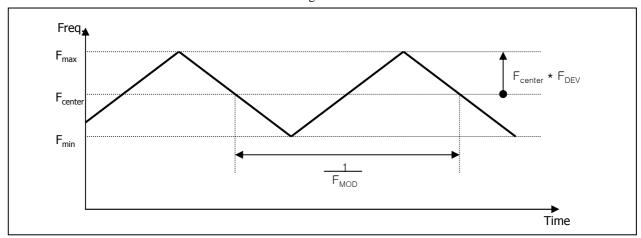
Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t _{SKEW}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
LVDS Clock to Data Skew Margin	t _{SKEW}	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{SKEW_EO}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-

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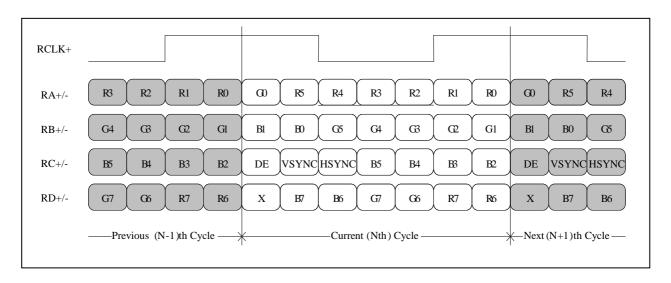
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

- LVDS 1 Port



< LVDS Data Format >

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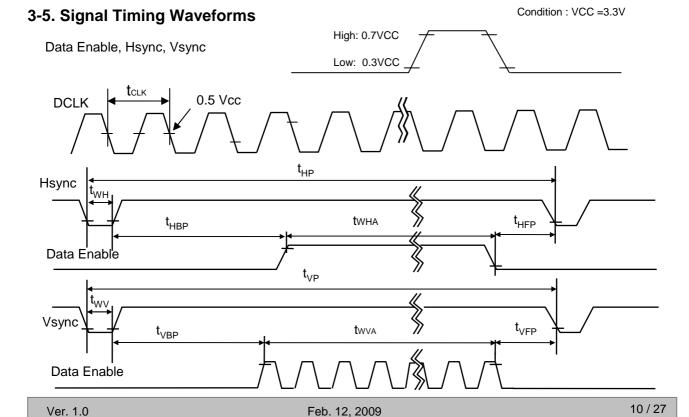


3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 5. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}	-	50.4	1	MHz	
	Period	Thp	1320	1344	1362		
Hsync	Width	t _{wh}	132	136	150	tCLK	
	Width-Active	t _{WHA}	1024	1024	1024		
	Period	t _{VP}	621	625	632		
Vsync	Width		1	3	5	tHP	
	Width-Active	t _{wva}	600	600	600		
	Horizontal back porch	t _{HBP}	144	160	160	tCLK	
Data	Horizontal front porch	t _{HFP}	20	24	28	ICLK	
Data _ Enable	Vertical back porch	t _{VBP}	20	22	24	+UD	
	Vertical front porch	t _{VFP}	0	50.4 - 1344 1362 136 150 1024 1024 625 632 3 5 600 600 160 160 24 28	tHP		





3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RE	D					GRE	EN					BL	UE		
`	30101	MSE	3				LSB	MSE	3				LSB	MSE	3				LSB
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В3	B 2	B 1	B 0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	.1	. 1	1		0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN																	· · · · · ·		
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	····· 1
BLUE					 												· · · · ·		
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	 1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	 1	1	 1	1	1
	. , ,	1																	



3-7. Power Sequence

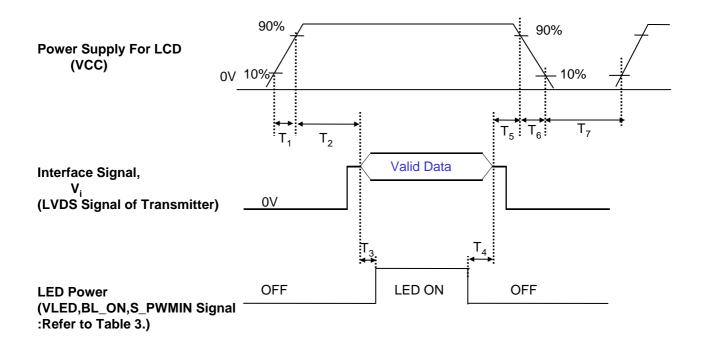


Table 7. POWER SEQUENCE TABLE

Parameter		Value		Units
	Min.	Тур.	Max.	
T ₁	0.5	-	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	-	-	(ms)
T ₄	200	-	-	(ms)
T ₅	0	-	50	(ms)
T ₆	0	-	10	(ms)
T ₇	400	-	-	(ms)

Note)

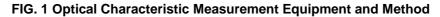
- 1. Valid Data is Data to meet "3-3. LVDS Signal Timing Specifications"
- 2. Please avoid floating state of interface signal at invalid period.
- 3. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 4. LED power must be turn on after power supply for LCD and interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.



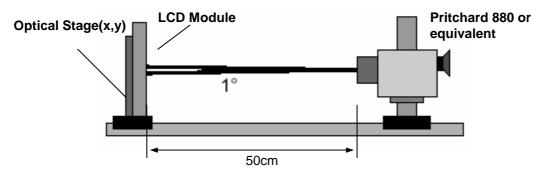


Table 8. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, f_{V} =60Hz, f_{CLK} = 62.6MHz, I_{BL} = 20 mA

Demonstra	0		Values		I I -: it -	Notes
Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio	CR	300	-	-]	1
Surface Luminance, white	L _{WH}	170	200	-	cd/m ²	2
Luminance Variation	δ_{WHITE}	.	1.4	1.6	1	3
Response Time	Tr_{R} + Tr_{D}	-	16	25	ms	4
Color Coordinates					1	
RED	RX	0.555	0.585	0.615	l	
	RY	0.319	0.349	0.379	[
GREEN	GX	0.306	0.336	0.366	[
	GY	0.528	0.558	0.588		
BLUE	BX	0.128	0.158	0.188	1	
	BY	0.095	0.125	0.155		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359	<u> </u>	
Viewing Angle					<u> </u>	5
x axis, right(Φ =0°)	Θr	30	-	-	degree	
x axis, left (Φ=180°)	Θl	30	-	-	degree	
y axis, up (Φ=90°)	Θu	10	-	-	degree	
y axis, down (Φ=270°)	Θd	20	-	-	degree	
Gray Scale			2.2			6

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Note)

1. Contrast Ratio(CR) is defined mathematically as

Contrast Ratio =

Surface Luminance with all black pixels

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = Average(L_1, L_2, \dots L_5)$$

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Maximum}(\textbf{L}_{1}, \textbf{L}_{2}, \ \dots \ \textbf{L}_{13})}{\text{Minimum}(\textbf{L}_{1}, \textbf{L}_{2}, \ \dots \ \textbf{L}_{13})}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$f_V = 60Hz$$

Gray Level	Luminance [%] (Typ)
LO	0.2
L7	2.07
L15	6.93
L23	14.0
L31	23.2
L39	36.4
L47	53.5
L55	75
L63	100

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FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

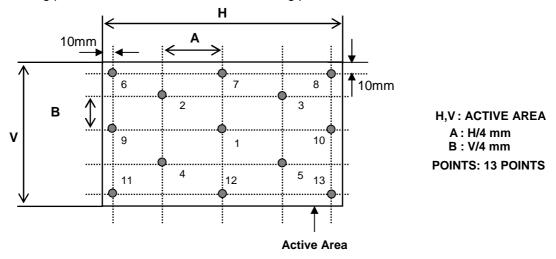


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

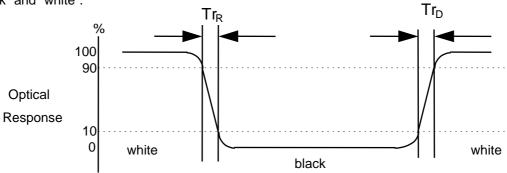
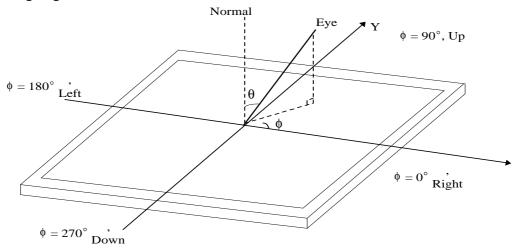


FIG. 4 Viewing angle



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5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP101WSA. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	235.0 ± 0.5 mm
Outline Dimension	Vertical	143.0 ± 0.5 mm
	Thickness	5.2mm (max)
Bezel Area	Horizontal	226.00 mm
bezei Alea	Vertical	128.70 mm
Active Display Area	Horizontal	222.72 mm
Active Display Area	Vertical	125.28 mm
Weight	190g (Max.)	
Surface Treatment	Glare treatment of the front polarize	ır

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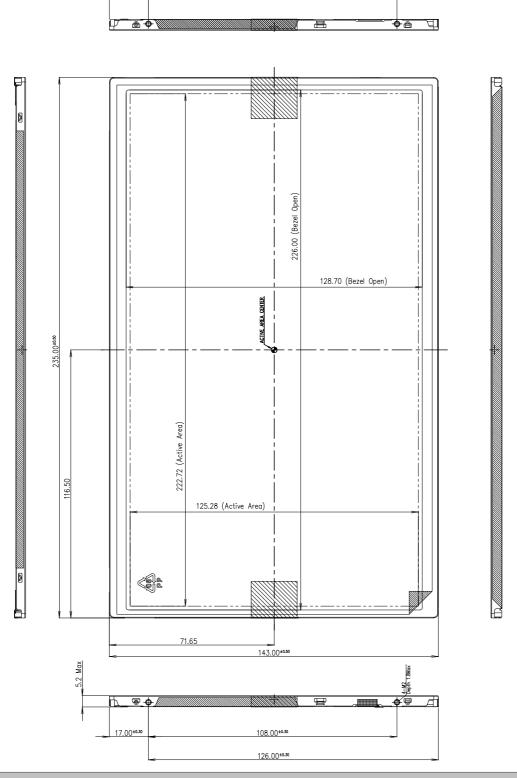


17.00±0.30

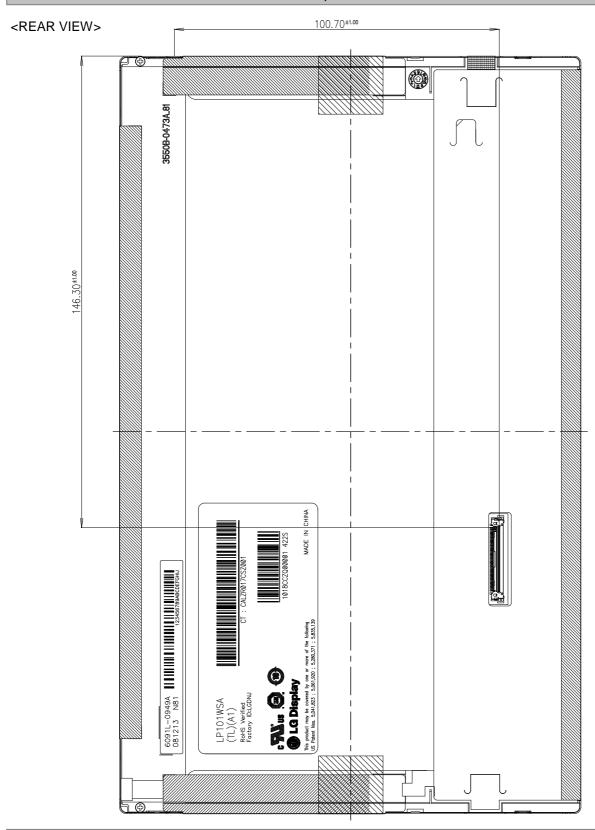
Product Specification

108.00±0.30

<FRONT VIEW>

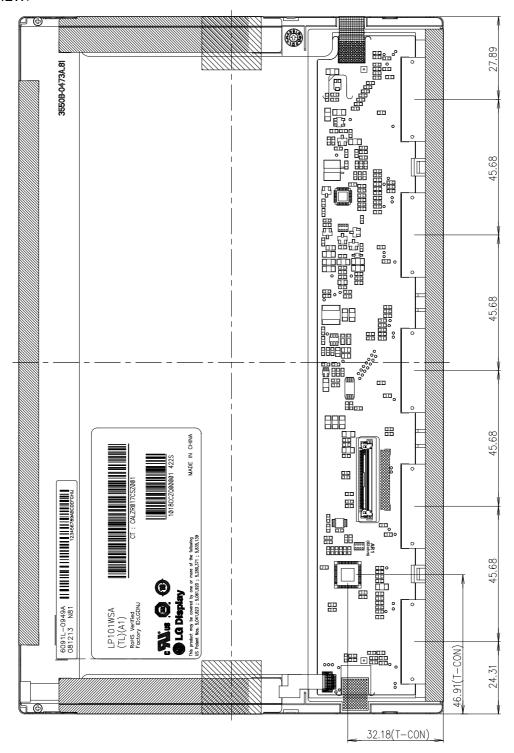








<REAR VIEW>

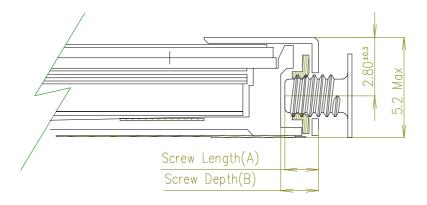


In order to avoid IC damage, it is not allowed to have such overlappings as cables or antennas, camera, WLAN, WWAN over these COF locations.

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[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



Section A-A

- * Mounting Screw Length (A) = 1.5(Min) /1.8(Max)
- * Mounting Screw Hole Depth (B) = 1.8(Min)
- * Mounting hole location : 2.8(typ.)
- * Torque : 2.0 kgf.cm(Max)

(Measurement gauge: torque meter)

Notes: 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.

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6. Reliability

Environment test condition

No.	Test Item	Conditions				
1	High temperature storage test	Ta= 60°C, 240h				
2	Low temperature storage test	Ta= -20°C, 240h				
3	High temperature operation test	Ta= 50°C, 50%RH, 240h				
4	Low temperature operation test	Ta= 0°C, 240h				
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis				
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 6ms for all six faces)				
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr				

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

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7. International Standards

7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc.,

Standard for Safety of Information Technology Equipment.

b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association,

Standard for Safety of Information Technology Equipment.

c) EN 60950-1:2001, First Edition,

European Committee for Electrotechnical Standardization(CENELEC)

European Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)

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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A,B,C : SIZE(INCH) D : YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 40 pcs

b) Box Size: 395mm × 390mm × 309mm

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9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

		Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
		0	00	Header	00	00000000
		1	01	Header	FF	111111111
		2	02	Header	FF	11111111
Header		3	03	Header	FF	11111111
		4	04	Header	FF	11111111
		5	05	Header	FF	11111111
		6	06	Header	FF	11111111
		7	07	Header	00	00000000
EDID		8	08	EISA manufacture code (3 Character ID) LGD	30	00110000
		9	09	EISA manufacture code (Compressed ASCⅡ)	E4	11100100
<u> </u>		10	0A	Panel Supplier Reserved - Product Code 01B9h	B9	10111001
		11	0В	(Hex. LSB first)	01	00000001
		12	0C	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
	Version	13	0D	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
	, s	14	0E	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
4		15	0F	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
		16	10	Week of Manufacture 00 weeks	00	00000000
opa		17	11	Year of Manufacture 2008 years	12	00010010
Vendor / Product		18	12	EDID structure version # = 1	01	00000001
		19	13	EDID revision # = 3	03	00000011
	7	20	14	Video input Definition = Digital signal	80	10000000
	Sta	21	15	Max H image size (Rounded cm) = 22 cm	16	00010110
	iet .	22	16	Max V image size (Rounded cm) = 13 cm	0D	00001101
Display	a a	23	17	Display gamma = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma	78	01111000
	Parameters	24	18	Feature Support (no_DPMS, no_Active Off/Very Low Power, RGB color display, Timing BLK 1,no_ GTF)	0A	00001010
	8	25	19	Red/Green Low Bits (RxRy/GxGy)	BF	10111111
	ng .	26	1A	Blue/White Low Bits (BxBy/WxWy)	B5	10110101
	rdi	27	1B	$Red X \qquad Rx = 0.588$	96	10010110
	00	28	1C	Red Y Ry = 0.347	58	01011000
	Ç	29	1D	Green X Gx = 0.331	54	01010100
,	<u> </u>	30	1E	Green Y Gy = 0.546	8B	10001011
	\ddot{c}	31	1F	Blue X Bx = 0.154	27	00100111
	<i>sel</i>	32	20	Blue Y By = 0.128	20	00100000
	Panel Color Coordinates	33	21	White X Wx = 0.313	50	01010000
		34	22	White Y Wy = 0.329 Established timing 1 (00h if not year)	54	01010100
1qx	uin ed	35	23	Established timing 1 (00h if not used)	00	00000000
Establ	ished Timin	36	24 25	Established timing 2 (00h if not used) Manufacturar's timings (00h if not used)	00	0000000
		37		Manufacturer's timings (00h if not used) Standard timing IDI (01h if not used)	00	00000001
		38 39	26	Standard timing ID1 (01h if not used) Standard timing ID1 (01h if not used)	01	00000001
		40	27	Standard timing ID1 (01h if not used) Standard timing ID2 (01h if not used)	01	00000001
		40	28	Standard timing ID2 (01h if not used) Standard timing ID2 (01h if not used)	01	00000001
		42	29 2A	Standard timing ID2 (01n it not used) Standard timing ID3 (01h if not used)	01	00000001
	7	43	2A 2B	Standard timing ID3 (01h if not used) Standard timing ID3 (01h if not used)	01	00000001
	ing	43	2B 2C	Standard timing ID3 (01h if not used) Standard timing ID4 (01h if not used)	01	00000001
	üm	45	2D	Standard timing ID4 (01h if not used) Standard timing ID4 (01h if not used)	01	00000001
	1	46	2E	Standard timing ID5 (01h if not used)	01	00000001
	ar	47	2F	Standard timing ID5 (01h if not used)	01	00000001
nd	48	30	Standard timing ID6 (01h if not used)	01	00000001	
Standard Timing ID		49	31	Standard timing ID6 (01h if not used)	01	00000001
	-4	50	32	Standard timing ID7 (01h if not used)	01	00000001
		51	33	Standard timing ID7 (01h if not used)	01	00000001
		52	34	Standard timing ID8 (01h if not used)	01	00000001



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	54	36	Pixel Clock/10,000 (LSB) 62.6 MHz @ 60Hz	74	00101100
	55	37	Pixel Clock/10,000 (MSB)	18	00010101
	56	38	Horizontal Active (lower 8 bits) 1024 Pixels	00	00000000
	57	39	Horizontal Blanking(Thp-HA) (lower 8 bits) 466 Pixels	D2	11010010
	58	3A	Horizontal Active / Horizontal Blanking(Thp-HA) (upper 4:4bits)	41	01000001
	59	3B	Vertical Avtive 600 Lines	58	01000000
#1	60	3C	Vertical Blanking (Tvp-HA) (DE Blanking typ.for DE only panels) 100 Lines	64	00011110
tor	61	3D	Vertical Active : Vertical Blanking (Tvp-HA) (upper 4:4bits)	20	00100000
τψ	62	3E	Horizontal Sync. Offset (Thfp) 48 Pixels	30	00110000
Timing Descriptor #1	63	3F	Horizontal Sync Pulse Width (HSPW) 32 Pixels	20	00100000
	64	40	Vertical Sync Offset(Tvfp): Sync Width (VSPW) 3 Lines: 6 Lines	36	00110101
Bu	65	41	Horizontal Vertical Sync Offset/Width (upper 2bits)	00	00000000
mi	66	42	Horizontal Image Size (mm) 224 mm	E 0	11100000
Ti.	67	43	Vertical Image Size (mm) 126 mm	7E	01111110
	68	44	Horizontal Image Size / Vertical Image Size	00	00000000
	69	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	70	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_NEG, Hsync_NEG), DE only note : LSB is set to '1' if panel is DE-timing only. H/V can be ignored.	19	00011001
	72	48	Flag	00	00000000
	73	49	Flag	00	00000000
	74	4A	Flag	00	00000000
	75	4B	Data Type Tag (Descriptor Defined by manufacturer)	00	00000000
	76	4C	Flag	00	00000000
Timing Descriptor #2	77	4D	Descriptor Defined by manufacturer	00	00000000
tor	78	4E	Descriptor Defined by manufacturer	00	00000000
τ i	79	4F	Descriptor Defined by manufacturer	00	00000000
3SCI	80	50	Descriptor Defined by manufacturer	00	00000000
Ď	81	51	Descriptor Defined by manufacturer	00	00000000
Bu	82	52	Descriptor Defined by manufacturer	00	00000000
mi	83	53	Descriptor Defined by manufacturer	00	00000000
Ti.	84	54	Descriptor Defined by manufacturer	00	00000000
	85	55	Descriptor Defined by manufacturer	00	00000000
	86	56	Descriptor Defined by manufacturer	00	00000000
	87	57	Descriptor Defined by manufacturer	00	00000000
	88	58	Descriptor Defined by manufacturer	00	00000000
	89	59	Descriptor Defined by manufacturer	00	00000000
	90	5A	Flag	00	00000000
	91	5B	Flag	00	
	92	5C	Flag Data Tuna Tag (ASCH String)	00	00000000
	93 94	5D	Data Type Tag (ASCII String)	FE 00	00000000
3		5E	Flag A SCH String		01001100
Timing Descriptor #3	95 96	5F 60	ASCII String L ASCII String G	4C 47	01000110
nto.				20	00100000
ļir.	97 98	61	ASCII String ASCII String D		01000100
8	99	63	ASCII String i	44 69	01101001
, E	100	64	ASCII String s	73	01110011
ing	101	65		70	01110000
ļ, m	101	66	ASCII String p ASCII String 1	6C	01101100
I	102	67	ASCII String a	61	01100001
	103	68	ASCII String y	79	01111001
	105	69	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah,set remaining char = 20h)	0A	00001010
	106	6A	Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h)	20	00100000
	107	6B	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah,set remaining char = 20h)	20	00100000
			, , , , , , , , , , , , , , , , , , , ,		



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	108	6C	Flag	00	00000000
	109	6D	Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag (Monitor Name, stored as ASCII)	FC	11111100
	112	70	Flag	00	00000000
#	113	71	Monitor Name, stored as ASCII L	4C	01001100
Timing Descriptor #4	114	72	Monitor Name, stored as ASCII P	50	01010000
ipt	115	73	Monitor Name, stored as ASCII 1	31	00110001
scr	116	74	Monitor Name, stored as ASCII 0	30	00110000
De	117	75	Monitor Name, stored as ASCII 1	31	00110001
0.0	118	76	Monitor Name, stored as ASCII W	57	01010111
nin	119	77	Monitor Name, stored as ASCII S	53	01010011
Ţ,	120	78	Monitor Name, stored as ASCII A	41	00110001
7.1	121	79	Monitor Name, stored as ASCII -	2D	00101101
	122	7A	Monitor Name, stored as ASCII T	54	01010100
	123	7B	Monitor Name, stored as ASCII L	4C	01001100
	124	7C	Monitor Name, stored as ASCII A	41	01000010
	125	7D	Monitor Name, stored as ASCII 1	31	00110010
Сћес	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
Ch	127	7 F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	24	11011100

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