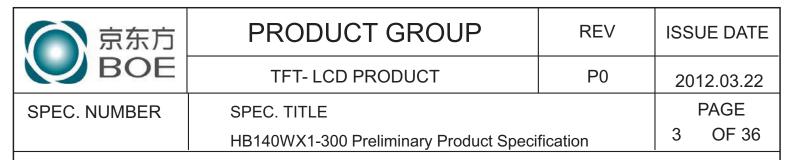


HB140WX1-300 **Preliminary Product Specification** Rev. P0

HEFEI BOE OPTOELECTRONICS TECHNOLOGY

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P0	-	Initial Release	2012.03.22	吕凤珍	
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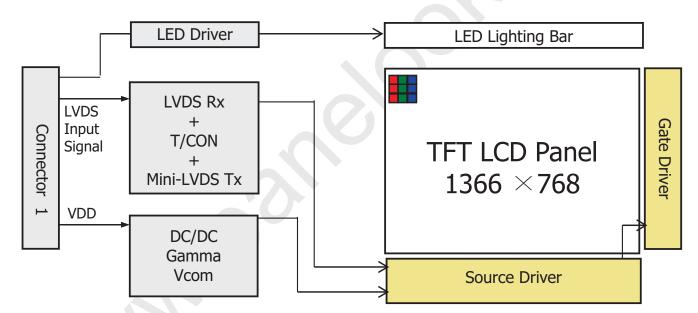
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1.0 GENERAL DESCRIPTION

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1.1 Introduction

HB140WX1-300 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 14.0 inch diagonally measured active area with WXGA resolutions (1366 horizontal by 768 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical Stripe and this module can display 262,144 colors. The TFT-LCD panel used for this module is a low reflection and higher color type. Therefore, this module is suitable for Notebook PC. The LED Driver for back-light driving is built in this model. All input signals are LVDS interface compatible.



1.2 Features

- 1 Channel LVDS Interface with 1 pixel / clock
- Thin and light weight
- 6-bit color depth, display 262K colors
- Single LED Lighting Bar. (Top side/Horizontal Direction)
- Data enable signal mode
- Up/Down Mounting Frame
- Green Product (RoHS & Halogen free product)
- On board LED Driving circuit
- Low driving voltage and low power consumption
- On board EDID chip



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1.3 Application

Notebook PC (Wide type)

1.4 General Specification

The followings are general specifications at the model HB140WX1-300. (listed in Table 1.)

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	309.4(H) ×173.95(V)	mm	
Number of pixels	1366 (H) ×768 (V)	pixels	
Pixel pitch	0.2265(H) ×0.2265 (V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	262K	colors	
Display mode	Normally White		
Dimensional outline	320.9(H)*187.6(V)*3.6(Max)	mm	
Weight	320 (max)	g	
Surface treatment	Hard-Coating 3H		
Back-light	Upper edge side, 1-LED Lighting Bar type		Note 1
Power consumption	P _D : 0.9 (max)	W	
	P _{BL} : 2.3 (max)	W	
	P _{total} : 3.2 (max)	W	

Notes: 1. LED Lighting Bar (36*LED Array)

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2.0 ABSOLUTE MAXIMUM RATINGS

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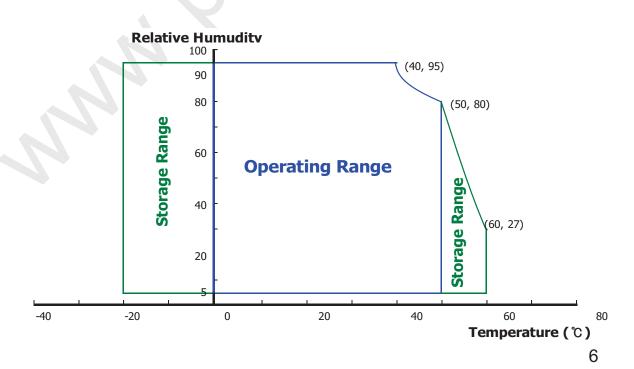
The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings>

Ta=25+/-2°C

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	-0.3	4.0	V	Note 1
Logic Supply Voltage	V _{IN}	V _{ss} -0.3	V _{DD} +0.3	V	Note i
Operating Temperature	T _{OP}	0	+50	$^{\circ}$ C	Note 2
Storage Temperature	T _{ST}	-20	+60	$^{\circ}\!\mathbb{C}$	Note 2

- Notes: 1. Permanent damage to the device may occur if maximum values are exceeded functional operation should be restricted to the condition described under normal operating conditions.
 - 2. Temperature and relative humidity range are shown in the figure below. 95 % RH Max. (40 °C ≥ Ta) Maximum wet - bulb temperature at 39 °C or less. (Ta > 40 °C) No condensation.



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3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

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< Table 3. Electrical specifications >

Ta=25+/-2°C

Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	3.0	3.3	3.6	V	Note 1
Permissible Input Ripple Voltage	V_{RF}	-	-	100	mV	At V _{DD} = 3.3V
Power Supply Current	I _{DD}	-	192) -	mA	Note 1
Positive-going Input Threshold Voltage	V _{IT+}	-		100	mV	\/ = 4.2\/ tvp
Negative-going Input Threshold Voltage	V _{IT-}	-100	-	-	mV	V _{cm} = 1.2V typ.
Differential Input Voltage	V _{ID}	200	-	600	mV	
	P_{D}	-	0.64	0.9	W	Note 1
Power Consumption	P_{BL}	-		2.3	W	Note 2
	P _{total}	-	2.84	3.2	W	

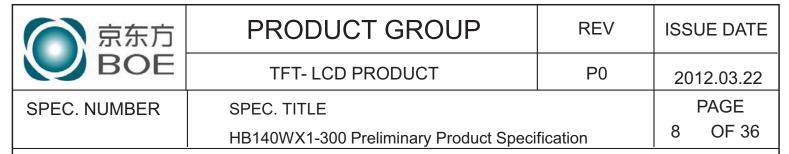
Notes: 1. The supply voltage is measured and specified at the interface connector of LCM. The current draw and power consumption specified is for 3.3V at 25°C.

a) Typ: Window XP pattern

b) Max: Vertical 2 line skip pattern



2. Calculated value for reference (VLED \times ILED)



3.0 ELECTRICAL SPECIFICATIONS

3.2 Backlight Unit

< Table 4. LED Driving guideline specifications > Ta=25+/-2°C

Table 4. LED Driving guideline specifications > 1a=25+/-2 C							
Parameter			Min.	Тур.	Max.	Unit	Remarks
LED Forward	Voltage	V _F	-	ı	3.0	V	-
LED Forward	Current	I _F	-	18.6		mA	-
LED Power C	Consumption	P _{LED}			2.3	W	Note 1
LED Life-Tim	е	N/A	15,000	-) -	Hour	I _F = 20mA
Power supply voltage for LED Driver		V _{LED}	6	12	21	V	
EN Control Level	Backlight on		2.0		5.0	V	
	Backlight off		0		1.0	V	
PWM Control	PWM High Level		2.0		5.0	V	
Level	PWM Low Level		0		0.1	V	
PWM Control Frequency		F _{PWM}	100	-	10,000	Hz	
Duty Ratio		-	1	ı	100	%	

Notes: 1. Power supply voltage12V for LED Driver, Driver efficiency 90%, Calculator Value for reference IF × VF ×36 / 0.9 = PLED

2. The LED Life-time define as the estimated time to 50% degradation of initial luminous.

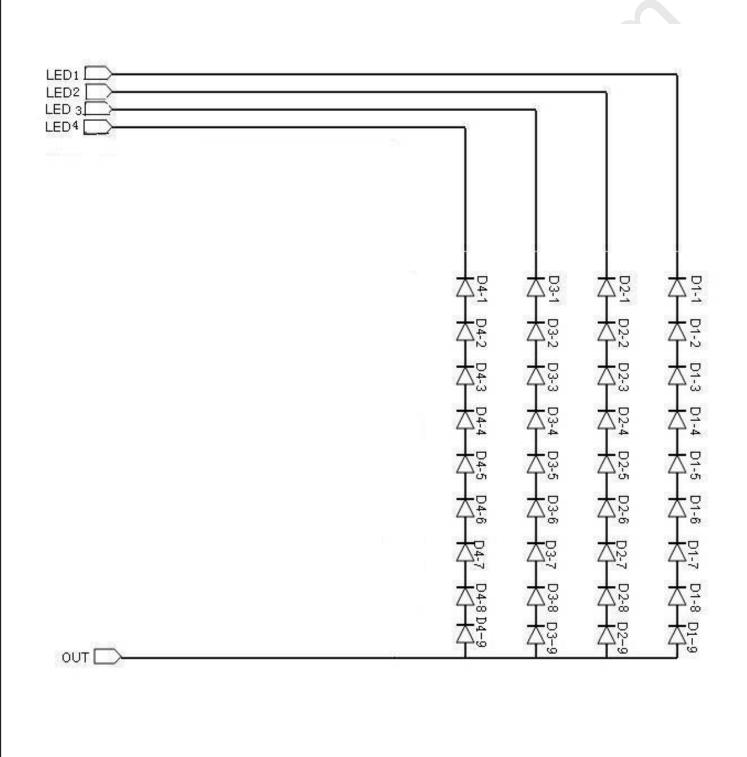
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3.0 ELECTRICAL SPECIFICATIONS

3.3 LED structure



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4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = 25±2°C) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0° . We refer to $\theta\emptyset=0$ $(=\theta3)$ as the 3 o'clock direction (the "right"), $\theta\emptyset=90$ (= $\theta12$) as the 12 o'clock direction ("upward"), $\theta\emptyset=180$ (= $\theta9$) as the 9 o'clock direction ("left") and $\theta \varnothing = 270 (= \theta 6)$ as the 6 o'clock direction ("bottom"). While scanning θ and/or \varnothing , the center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement. VDD shall be 3.3+/- 0.3V at 25°C. Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

<Table 5 Ontical Specifications:

< Table 5. Optical Specifications>								
Paramo	eter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	Horizontal	Θ_3		40	45	-	Deg.	
Viewing Angle	Horizoniai	Θ_9	CR > 10	40	45	-	Deg.	Note 1
range	Vertical	Θ ₁₂	CK > 10	15	20	-	Deg.	Note i
	Vertical	Θ_6		30	45	-	Deg.	
Luminance Co	ntrast ratio	CR	Θ = 0°	500	600			Note 2
Luminance of White	5 Points	Y _w	Θ = 0°	170	200	-	cd/m ²	Note 3
White	5 Points	ΔΥ5	ILED = 20mA	80	-	-		
Luminance uniformity	13 Points	ΔΥ13		65	-	-		Note 4
White Chro	maticity	X _w	Θ = 0°	0.283	0.313	0.343		Note 5
vville Cillo	maticity	y _w		0.299	0.329	0.359		INOIG 3
	Red	X _R			0.592			<u> </u>
	1100	y _R			0.347			
Reproduction	Green	X _G	Θ = 0°	-0.03	0.329	+0.03		
of color	010011	y _G	0 - 0	-0.03	0.571	+0.03		
	Blue	X _B			0.151			
	Diue	y _B			0.115			
Response (Rising + F		T _{RT}	Ta= 25° C Θ = 0°	-	12	16	ms	Note 6
Cross 7	Гalk	СТ	⊝ = 0°	-	-	2.0	%	Note 7

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Notes:

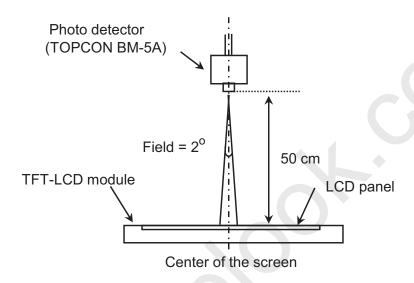
- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).
- 2. Contrast measurements shall be made at viewing angle of Θ = 0 and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (see FIGURE 1) Luminance Contrast Ratio (CR) is defined mathematically.

- 3. Center Luminance of white is defined as luminance values of 5 point average across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 4. The White luminance uniformity on LCD surface is then expressed as : ΔY =Minimum Luminance of 5(or 13) points / Maximum Luminance of 5(or 13) points. (see FIGURE 2 and FIGURE 3).
- 5. The color chromaticity coordinates specified in Table 5 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 6. The electro-optical response time measurements shall be made as FIGURE 4 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Td.
- 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark. (See FIGURE 5).

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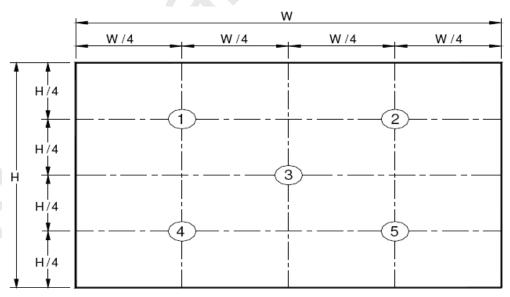
4.3 Optical measurements

Figure 1. Measurement Set Up



Optical characteristics measurement setup

Figure 2. White Luminance and Uniformity Measurement Locations (5 points)



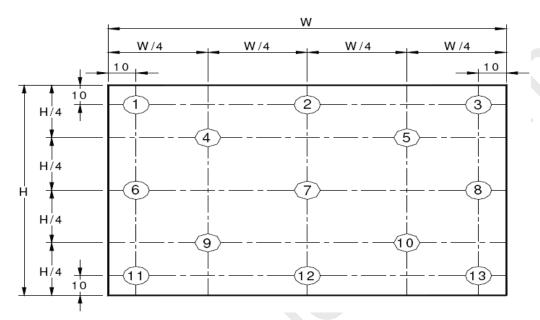
Center Luminance of white is defined as luminance values of center 5 points across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.

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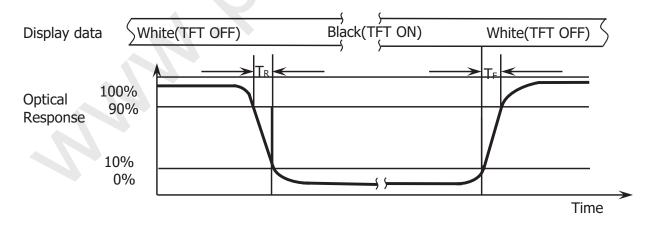
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Figure 3. Uniformity Measurement Locations (13 points)



The White luminance uniformity on LCD surface is then expressed as : $\Delta Y5 =$ Minimum Luminance of five points / Maximum Luminance of five points (see FIGURE 2), ΔY13 = Minimum Luminance of 13 points /Maximum Luminance of 13 points (see FIGURE 3).

Figure 4. Response Time Testing



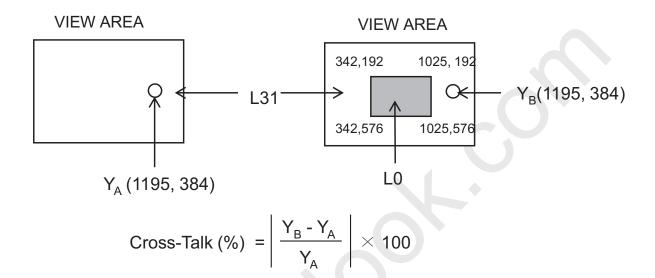
The electro-optical response time measurements shall be made as shown in FIGURE 4 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td and 90% to 10% is Tr.

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Figure 5. Cross Modulation Test Description



Where:

 Y_A = Initial luminance of measured area (cd/m²)

Y_B = Subsequent luminance of measured area (cd/m²)

The location measured will be exactly the same in both patterns

Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark (Refer to FIGURE 5).



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5.0 INTERFACE CONNECTION.

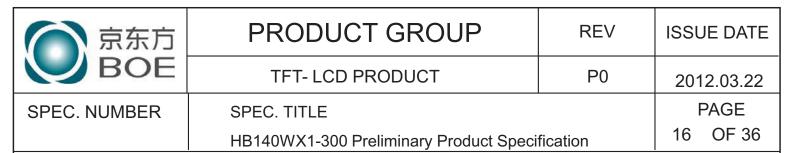
5.1 Electrical Interface Connection

The electronics interface connector is STM or Compatible or equivalent. The mating connector part number is I-PEX 20455-040T-11 or Compatible. The connector interface pin assignments are listed in Table 6.

|--|--|--|--|

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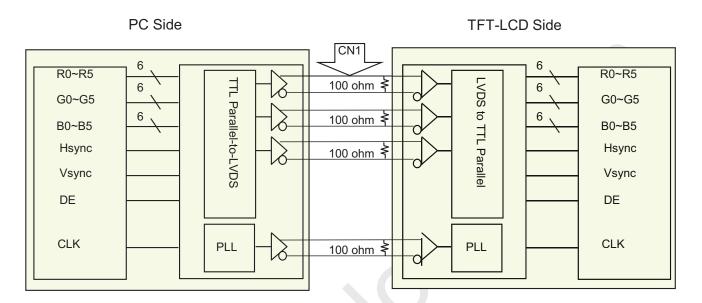
Terminal	Symbol	Functions
Pin No.	Symbol	Description
31	VLED_GND	LED Ground
32	VLED_GND	LED Ground
33	VLED_GND	LED Ground
34	NC	No Connection
35	PWM	System PWM Signal Input
36	LED_EN	LED enable pin(+3.3V Input)
37	CABC	CABC enable pin (0:Bypass;1 Enable)
38	VLED	LED Power Supply 6V-21V
39	VLED	LED Power Supply 6V-21V
40	VLED	LED Power Supply 6V-21V



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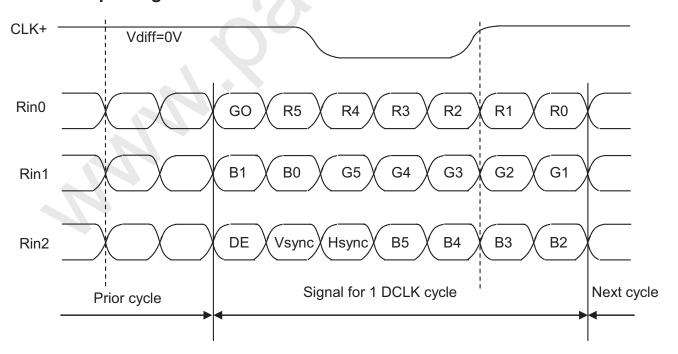
5-2. LVDS Interface

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Note. Transmitter: Thine THC63LVDM63A or equivalent. Transmitter is not contained in Module.

5.3.LVDS Input signal



Note. Pin connection in case of using Thine THC63LVDM63A

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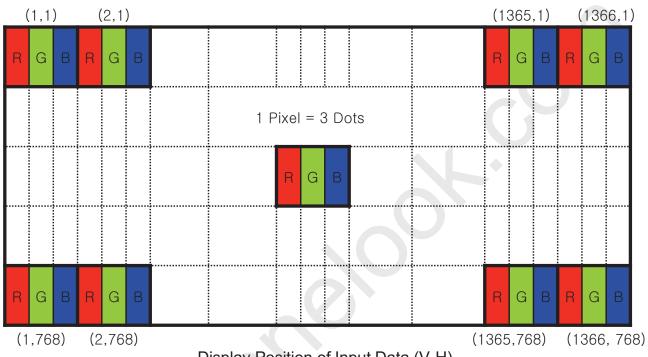


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5.3 Data Input Format

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<Table 6. Pin Assignments for the Interface Connector>



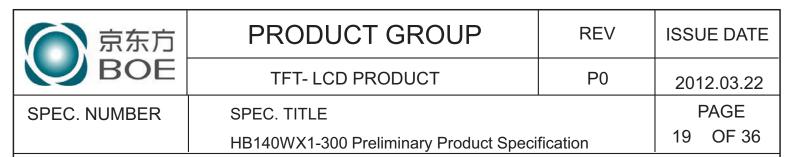
Display Position of Input Data (V-H)

5.4 Back-light & LCM Interface Connection

Interface Connector: MS24022P10 or Equivalent

<Table 7. Pin Assignments for the BLU & LCM Connector>

Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	LED1	LED cathode connection	6	NC	No Connection
2	LED2	LED cathode connection	7	NC	No Connection
3	LED3	LED cathode connection	8	Vout	LED anode connection
4	LED4	LED cathode connection	9	Vout	LED anode connection
5	NC	No Connection	10	Vout	LED anode connection



6.0 SIGNAL TIMING SPECIFICATION

6.1 The HB140WX1-300 is operated by the DE only.

Item		Symbols	Min	Тур	Max	Unit
	Frequency	1/Tc	67.5	72.3	76.3	MHz
Clock	High Time	Tch	-	4/7	-	Tc
	Low Time	Tcl	-	3/7	-	Tc
	Frame Period		778	790	802	lines
Fra			-	60	-	Hz
			-	16.7	-	ms
Vertical Display Period		Tvd	768	768	768	lines
One line Scanning Period		Th	1446	1526	1586	clocks
Horizontal Display Period		Thd	1366	1366	1366	clocks



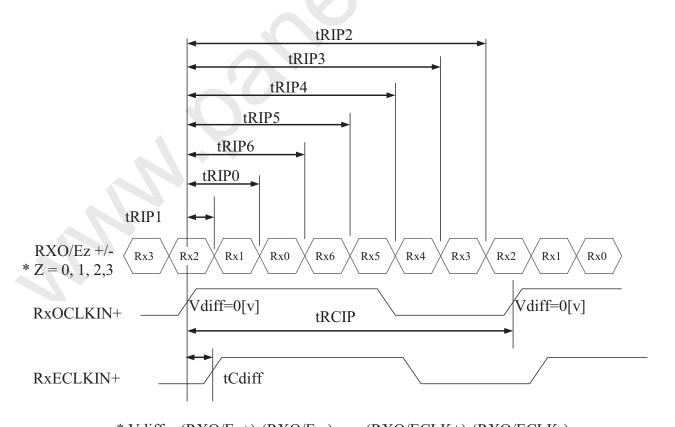
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6.2 LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter is shown in Table 8.

<Table 8. LVDS Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
CLKIN Period	tRCIP	-	13.83	25	nsec	
CLK Difference	tCdiff	-tRCIP*(3/7)	0	+tRCIP*(3/7)	nsec	
Input Data 0	tRIP1	-0.4	0.0	+0.4	nsec	
Input Data 1	tRIP0	tRICP/7-0.4	tRICP/7	tRICP/7+0.4	nsec	
Input Data 2	tRIP6	2 ×tRICP/7-0.4	2 ×tRICP/7	$2 \times tRICP/7+0.4$	nsec	
Input Data 3	tRIP5	3 ×tRICP/7-0.4	3 ×tRICP/7	$3 \times tRICP/7+0.4$	nsec	
Input Data 4	tRIP4	4 ×tRICP/7-0.4	4 ×tRICP/7	$4 \times tRICP/7 + 0.4$	nsec	
Input Data 5	tRIP3	5 ×tRICP/7-0.4	5 ×tRICP/7	$5 \times tRICP/7 + 0.4$	nsec	
Input Data 6	tRIP2	6 ×tRICP/7-0.4	6 × tRICP/7	6 × tRICP/7+0.4	nsec	



* Vdiff = (RXO/Ez+)-(RXO/Ez-), ..., (RXO/ECLK+)-(RXO/ECLK-)

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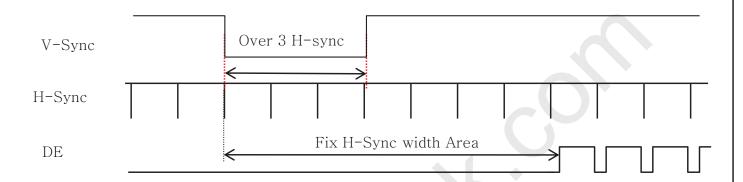


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7.0 SIGNAL TIMING WAVEFORMS OF INTERFACE SIGNAL

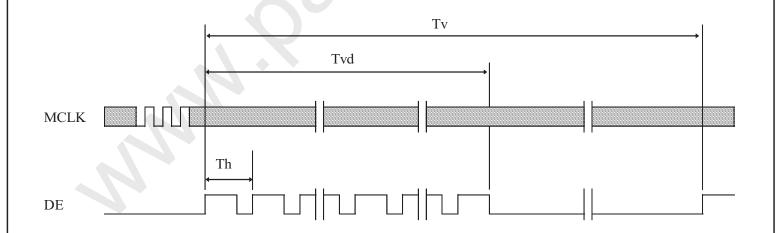
7.1 Sync Timing Waveforms

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- 1) Need over 3 H-sync during V-Sync Low
- 2) Fix H-Sync width from V-Sync falling edge to first rising edge

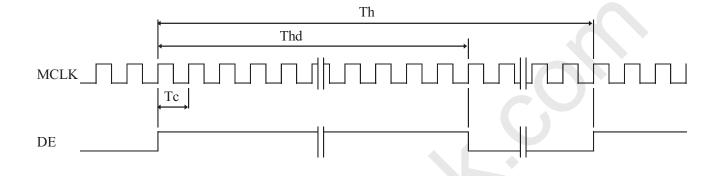
7.2 Vertical Timing Waveforms

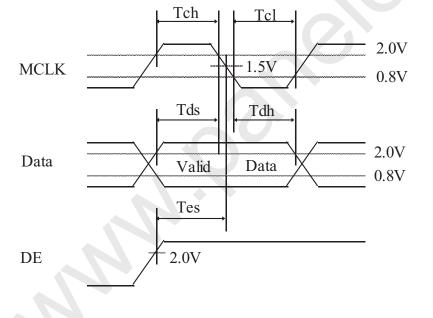


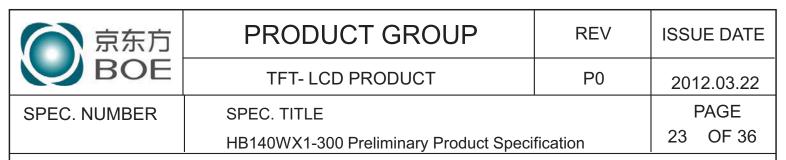


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7.3 Horizontal Timing Waveforms







8.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

	Colors &		Data signal	
	Gray scale	R0 R1 R2 R3 R4 R5	G0 G1 G2 G3 G4 G5	B0 B1 B2 B3 B4 B5
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0
	Blue	0 0 0 0 0 0	0 0 0 0 0 0	1 1 1 1 1 1
Basic	Green	0 0 0 0 0 0	1 1 1 1 1 1	0 0 0 0 0 0
colors	Light Blue	0 0 0 0 0 0	1 1 1 1 1 1	1 1 1 1 1 1
	Red	1 1 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0
	Purple	1 1 1 1 1 1	0 0 0 0 0 0	1 1 1 1 1 1
	Yellow	1 1 1 1 1 1	1 1 1 1 1 1	0 0 0 0 0 0
	White	1 1 1 1 1 1	1 1 1 1 1 1	1 1 1 1 1 1
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0
	Δ	1 0 0 0 0 0	0 0 0 0 0	0 0 0 0 0 0
	Darker	0 1 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0
Gray scale	Δ	↑	1	↑
of Red	riangleright	↓	1	\downarrow
	Brighter	1 0 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0
	∇	0 1 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0
	Red	1 1 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0
	Δ	0 0 0 0 0 0	1 0 0 0 0 0	0 0 0 0 0 0
	Darker	0 0 0 0 0 0	0 1 0 0 0 0	0 0 0 0 0 0
Gray scale		1	↑	↑
of Green	∇	1	↓	↓
	Brighter	0 0 0 0 0 0	1 0 1 1 1 1	0 0 0 0 0
	riangle	0 0 0 0 0 0	0 1 1 1 1 1	0 0 0 0 0 0
	Green	0 0 0 0 0 0	1 1 1 1 1 1	0 0 0 0 0 0
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0
		0 0 0 0 0 0	0 0 0 0 0 0	1 0 0 0 0 0
	Darker	0 0 0 0 0 0	0 0 0 0 0 0	0 1 0 0 0 0
Gray scale	Δ	<u> </u>	↓	<u>†</u>
of Blue	∇	↓	↓	↓
	Brighter	0 0 0 0 0 0	0 0 0 0 0 0	1 0 1 1 1 1
	∇	0 0 0 0 0 0	0 0 0 0 0 0	0 1 1 1 1 1
	Blue	0 0 0 0 0 0	0 0 0 0 0 0	1 1 1 1 1 1
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0
Gray		1 0 0 0 0 0	1 0 0 0 0 0	1 0 0 0 0 0
scale	Darker	0 1 0 0 0 0	0 1 0 0 0 0	0 1 0 0 0 0
of		<u> </u>	<u> </u>	<u> </u>
White		<u></u>	↓	↓
&	Brighter	1 0 1 1 1 1	1 0 1 1 1 1	1 0 1 1 1 1
Black	∇	0 1 1 1 1 1	0 1 1 1 1 1	0 1 1 1 1 1
	White	1 1 1 1 1 1	1 1 1 1 1 1	1 1 1 1 1 1

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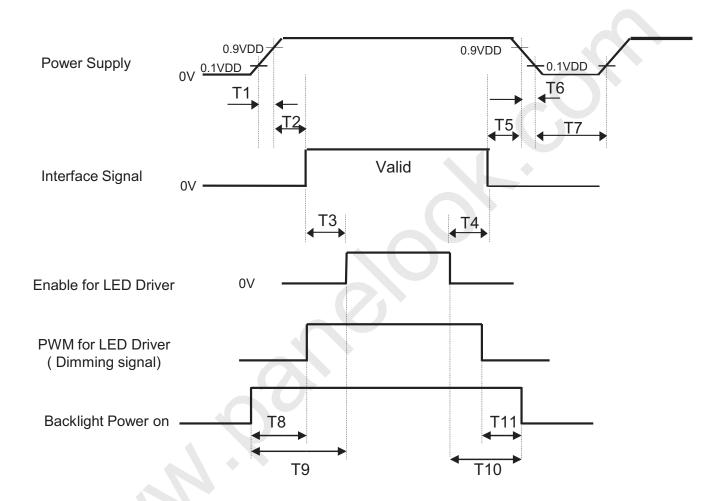


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9.0 POWER SEQUENCE

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To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



- 0.5ms \leq T1 \leq 10 ms
- 0 ms \leq T2 \leq 50 ms
- $200 \, \text{ms} \leq T3$
- 0 ms < T4
- 0ms ≤ T5

- \leq T6 \leq 10 ms 0 ms
- $150 \text{ms} \leq T7$
- 0 ms ≤ T8
- ≤ T9 0 ms
- ≤ T10 0ms
- ≤ T11 0ms

Notes:

- 1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on. Back Light must be turn on after power for logic and interface signal are valid.



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10.0 Connector Description

Physical interface is described as for the connector on LCM. These connectors are capable of accommodating the following signals and will be following components.

10.1 TFT LCD Module

Connector Name /Description	For Signal Connector
Manufacturer	STM or Compatible
Type/ Part Number	MSAK24025P40G or Compatible
Mating housing/ Part Number	I-PEX 20455-040T-11 or Compatible

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11.0 MECHANICAL CHARACTERISTICS

11.1 Dimensional Requirements

FIGURE 6 shows mechanical outlines for the model HB140WX1-300. Other parameters are shown in Table 9.

<Table 9. Dimensional Parameters>

Parameter	Specification	Unit
Active Area	309.40 (H) ×173.95 (V)	
Number of pixels	1366 (H) X 768 (V) (1 pixel = R + G + B dots)	
Pixel pitch	0.2265 (H) X 0.2265 (V)	
Pixel arrangement	RGB Vertical stripe	
Display colors	262K	
Display mode	Normally white	
Dimensional outline	320.9(H)*187.6(V)*3.6(Max)	mm
Weight	320 (max)	gram
Pools Light	Connector: MS24022P10	
Back Light —	LED, Horizontal-LED Array type	

10.2 Mounting

See FIGURE 6.

10.3 Glare and Polarizer Hardness.

The surface of the LCD has an glare coating to maximize readability and hard coating to reduce scratching.

10.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

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12.0 RELIABILITY TEST

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The Reliability test items and its conditions are shown in below.

<Table 10. Reliability test>

No	Test Items	Conditions		
1	High temperature storage test	Ta = 60 ℃, 240 hrs		
2	Low temperature storage test	Ta = -20 ℃, 240 hrs		
3	High temperature & high humidity operation test	Ta = 50 ℃, 80%RH, 240 hrs		
4	High temperature operation test	Ta = 50 ℃, 240 hrs		
5	Low temperature operation test	Ta = 0 °C, 240 hrs		
6	Thermal shock	Ta = -20 $^{\circ}$ C \leftrightarrow 60 $^{\circ}$ C (0.5 hr), 100 cycle		
7	Vibration test (non-operating)	1.5G, 10~500Hz,Half Sine X,Y,Z / Sweep rate : 1 hour		
8	Shock test (non-operating)	220G, Half Sine Wave 2msec \pm X, \pm Y, \pm Z Once for each direction		
9	Electro-static discharge test (non-operating)	Air : 150 pF, 330Ω, 15 KV Contact : 150 pF, 330Ω, 8 KV		

13.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
 - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
 - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD module is operating.
 - Put the module display side down on a flat horizontal plane.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.

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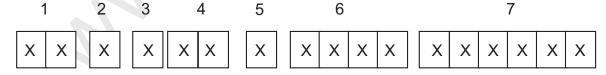
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- (4) Cautions for the atmosphere
 - Dew drop atmosphere should be avoided.
 - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
 - Do not apply fixed pattern data signal to the LCD module at product aging.
 - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
 - Do not disassemble and/or re-assemble LCD module.
 - Do not re-adjust variable resistor or switch etc.
 - When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

14.0 LABEL

(1) Product label





Type designation

No 1. Control Number

No 2. Rank / Grade

No 3. Line classification

No 4. Year (10: 2010, 11: 2011, ...)

No 5. Month (1, 2, 3, ..., 9, X, Y, Z)

No 6. Product Identification (FG)

No 7. Serial Number

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(2) High voltage caution label

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HIGH VOLTAGE CAUTION

RISK OF ELECTRIC SHOCK, DISCONNECT THE ELECTRIC POWER BEFORE SERVICING

COLD CATHODE FLUORESCENT LAMP IN LCD PANEL CONTAINS A SMALL AMOUNT OF MERCURY, PLEASE FOLLOW LOCAL OR-DINANCES OR REGULATIONS FOR DISPOSAL.

(3) Box label

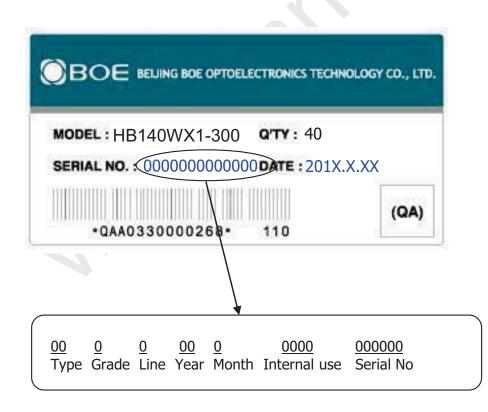
Label Size: 110 mm (L) \times 56 mm (W)

Contents

Model: HB140WX1-300 Q'ty: Module Q'ty in one box

Serial No.: Box Serial No. See next figure for detail description.

Date: Packing Date Internal use of Product

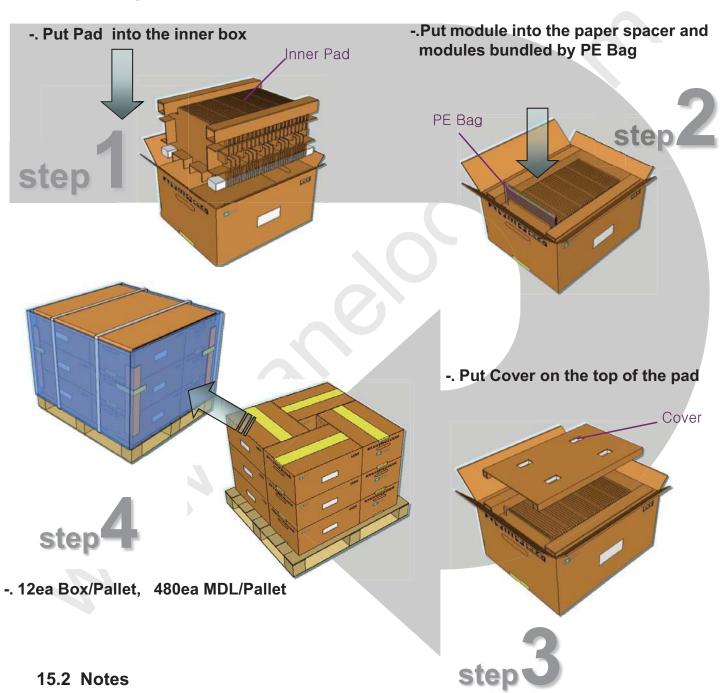




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15.0 PACKING INFORMATION

15.1 Packing order



- Box Dimension: 580mm(W) x 450mm(D) x 280mm(H)
- Package Quantity in one Box: 40pcs
- Total Weight: 15kg

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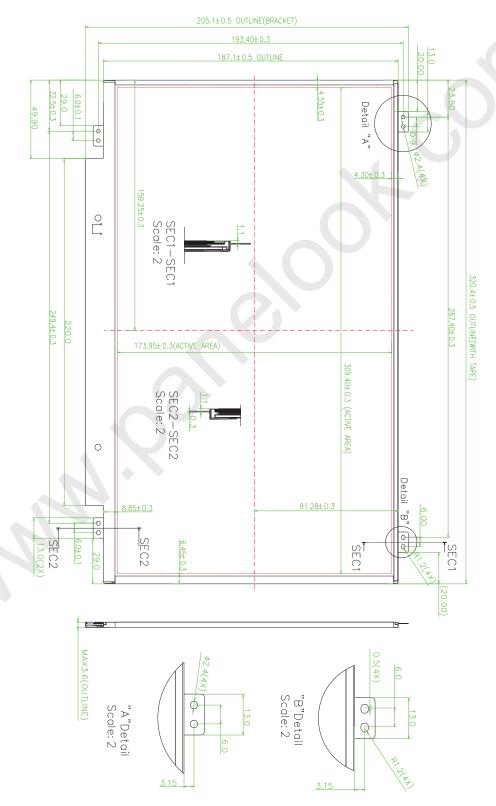


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16.0 MECHANICAL OUTLINE DIMENSION

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Figure 6. TFT-LCD Module Outline Dimension (Front View)

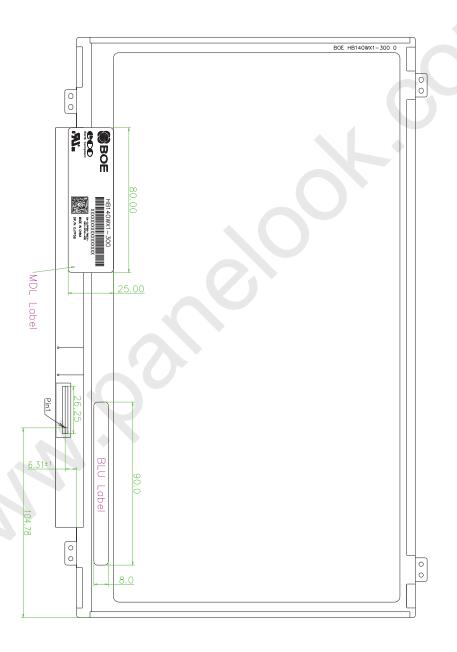


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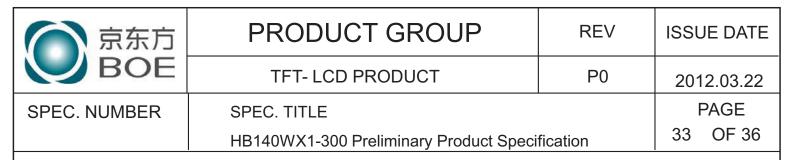
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Figure 7. TFT-LCD Module Outline Dimensions (Rear view)







17.0 EDID Table

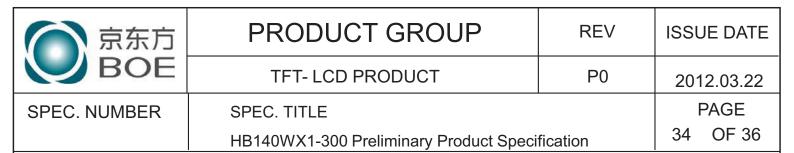
Address (HEX)	Function	Hex	Dec	Input values.	Notes
00		00	0	0	
01		FF	255	255	
02		FF	255	255	
03	Hoodor	FF	255	255	EDID Header
04	Header	FF	255	255	EDID Header
05		FF	255	255	
06		FF	255	255	
07		00	0	0	
08	ID Manufacturer	09	9	BOE	ID - BOE
09	Name	E5	229	BOE	ID = BOE
0A	ID Broduct Code	C3	195	1474	ID = 1474
0B	ID Product Code	05	5	14/4	ID = 1474
0C		00	0		
0D	32-bit serial No.	00	0		
0E	32-bit serial No.	00	0		
0F		00	0		
10	Week of manufacture	01	1	1	
11	Year of Manufacture	16	22	2012	Manufactured in 2012
12	EDID Structure Ver.	01	1	1	EDID Ver 1.0
13	EDID revision #	04	4	4	EDID Rev. 0.4
14	Video input definition	80	128	-	
15	Max H image size	1F	31	31	31 cm (Approx)
16	Max V image size	11	17	17	17 cm (Approx)
17	Display Gamma	78	120	2.2	Gamma curve = 2.2
18	Feature support	0A	10		RGB display, Preferred Timming mode
19	Red/Green low bits	В0	176	-	Red / Green Low Bits
1A	Blue/White low bits	90	144	-	Blue / White Low Bits
1B	Red x high bits	97	151	0.592	Red (x) = 10010111 (0.592)
1C	Red y high bits	58	88	0.347	Red (y) = 01011000 (0.347)
1D	Green x high bits	54	84	0.329	Green (x) = 01010100 (0.329)
1E	Green y high bits	92	146	0.571	Green (y) = 10010010 (0.571)
1F	Blue x high bits	26	38	0.151	Blue (x) = 00100110 (0.151)
20	BLue y high bits	1D	29	0.115	Blue (y) = 00011101 (0.115)
21	White x high bits	50	80	0.313	White (x) = 01010000 (0.313)
22	White y high bits	54	84	0.329	White (y) = 01010100 (0.329)
23	Established timing 1	00	0	-	
24	Established timing 2	00	0	-	

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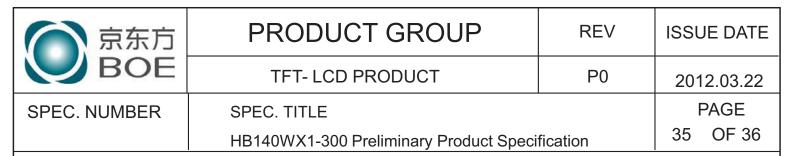


25	Established timing 3	00	0	-	
26		01	1		
27	Standard timing #1	01	1		Not Used
28	Standard timing #2	01	1		
29		01	1		Not Used
2A	01 1 11: 110	01	1		N II
2B	Standard timing #3	01	1		Not Used
2C	Otana da nalitina in an #4	01	1		Nethers
2D	Standard timing #4	01	1		Not Used
2E	Otana da nel timaina e #5	01	1		Netheral
2F	Standard timing #5	01	1		Not Used
30	Otana da nalitina in a 40	01	1		N-411I
31	Standard timing #6	01	1		Not Used
32	Cton dowd time in a #7	01	1		NetUsed
33	Standard timing #7	01	1		Not Used
34	Ctandard timing #0	01	1		Not Used
35	Standard timing #8	01	1		Not Osed
36		E4	228	71.4	71.4MHz Main clock
37		1B	27	/1.4	7 1.4IVII 12 IVIAIIT CIOCK
38		56	86	1366	Hor Active = 1366
39		7A	122	122	Hor Blanking = 122
3A		50	80	-	4 bits of Hor. Active + 4 bits of Hor. Blanking
3B		00	0	768	Ver Active = 768
3C		20	32	32	Ver Blanking = 32
3D		30	48	-	4 bits of Ver. Active + 4 bits of Ver. Blanking
3E	Detailed timing/monitor	30	48	48	Hor Sync Offset = 48
3F	descriptor #1	20	32	32	H Sync Pulse Width = 32
40		36	54	3	V sync Offset = 3 line
41]	00	0	6	V Sync Pulse width : 6 line
42	_	35	53	309	Horizontal Image Size = 309 mm (Low 8 bits)
43		AD	173	173	Vertical Image Size = 173 mm (Low 8 bits)
44]	10	16	-	4 bits of Hor Image Size + 4 bits of Ver Image Size
45]	00	0	0	Hor Border (pixels)
46		00	0	0	Vertical Border (Lines)
47		1A	26		Refer to right table

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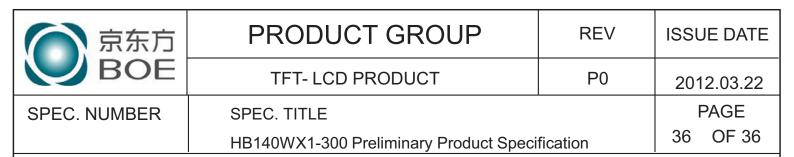
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48		30	48	F.C. 0	50 00411 04 1 1 1
49		16	22	56.8	56.8MHz Main clock
4A		56	86	1366	Hor Active = 1366
4B		00	0	256	Hor Blanking = 256
4C		51	81	-	4 bits of Hor. Active + 4 bits of Hor. Blanking
4D		00	0	768	Ver Active = 768
4E		6C	108	108	Ver Blanking = 108
4F		30	48	-	4 bits of Ver. Active + 4 bits of Ver. Blanking
50	Detailed	30	48	48	Hor Sync Offset = 48
51	timing/monitor descriptor #2	20	32	32	H Sync Pulse Width = 32
52	,	36	54	3	V sync Offset = 3 line
53		00	0	6	V Sync Pulse width : 6 line
54		35	53	309	Horizontal Image Size = 309 mm (Low 8 bits)
55		AD	173	173	Vertical Image Size = 173 mm (Low 8 bits)
56		10	16	-	4 bits of Hor Image Size + 4 bits of Ver Image Size
57		00	0	0	Hor Border (pixels)
58		00	0	0	Vertical Border (Lines)
59		1A	26		
5A		00	0		
5B		00	0		
5C		00	0		ASCII Data Sting Tag
5D		FE	254		
5E		00	0		
5F		4A	74	J	
60		37	55	7	
61		50	80	Р	D/PN:J7P58
62	Detailed timing/monitor	35	53	5	
63	descriptor #3	38	56	8	
64		80	128	10000000	EDID:A00
65		48	72	Н	
66		42	66	В	
67		31	49	1	
68		34	52	4	BOE PN: HB140WX1-300
69		33	51	3	
6A		30	48	0	
6B		30	48	0	





6C		00	0		
6D		00	0		
6E		00	0		Product Name Tag (ASCII)
6F		00	0		
70		00	0		
71		00	0	00000000	6-bit Color Depth & no FRC
72		41	65	01000001	WLED & singal light bar & one light bar
73		01	1	00000001	Frame rate 40Hz~65Hz
74	Detailed	94	148	10010100	Light Controller: PWM & Max. Luminance 200
75	timing/monitor descriptor #4	01	1	00000001	Front Surface: Glare & RGB v-stripe
76		10	16	00010000	With DBC
77		00	0	00000000	no Motion Blur & no Active Gamma
78		00	0	00000000	no Wireless Enhancement & no In-Cell Scanner
79		01	1	00000001	Single LVDS
7A		01	1	00000001	Built-In Self Test
7B		0A	10		
7C		20	32		
7D		20	32		
7E	Extension flag	00	0		
7F	Checksum	6A	106	106	